

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Osamu NAKAMURA

Serial No.: 10/809,118

Filed: March 25, 2004

For: Semiconductor Device And
Manufacturing Method Thereof

Examiner: Hoai V. Pham

Confirm No.: 7733

Art Unit: 2814

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ELECTION AND AMENDMENT B

In response to the Restriction Requirement of September 17, 2007, Applicant elects to prosecute Group I, directed to Claims 1-13 and 21-25.

Applicants are making this election and canceling the non-elected claims without disclaimer or prejudice to later filing a divisional application on the non-elected claims.

Please enter the following amendment in the above-identified application: